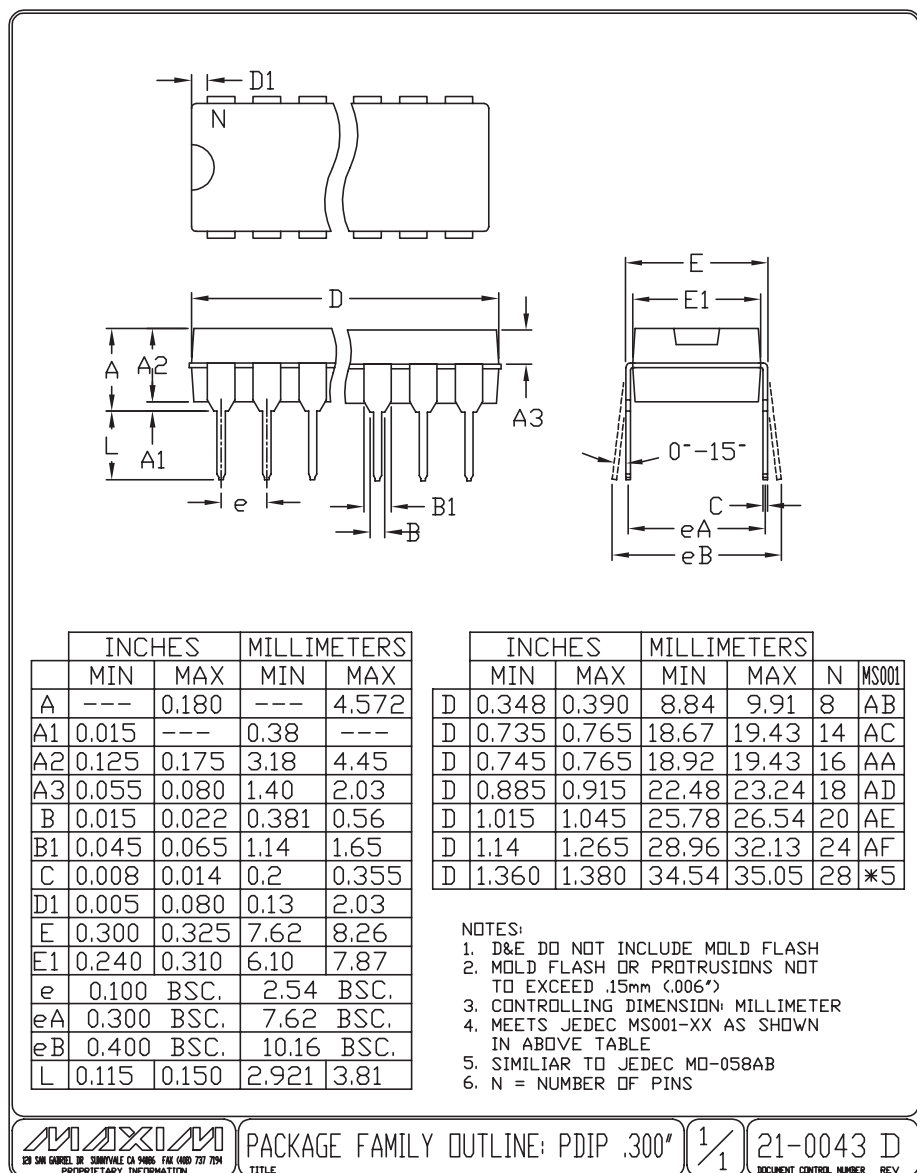


High-Speed, Low-Power, 3V/5V, Rail-to-Rail, Single-Supply Comparators

Package Information (continued)

(For the latest package outline information and land patterns, go to www.maxim-ic.com/packages.)



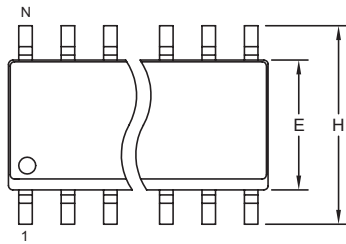
High-Speed, Low-Power, 3V/5V, Rail-to-Rail, Single-Supply Comparators

Package Information (continued)

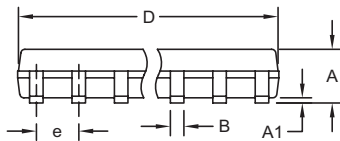
(For the latest package outline information and land patterns, go to www.maxim-ic.com/packages.)

MAX941/MAX942/MAX944

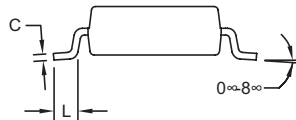
SOICN.EPS



TOP VIEW



FRONT VIEW



SIDE VIEW

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.053	0.069	1.35	1.75
A1	0.004	0.010	0.10	0.25
B	0.014	0.019	0.35	0.49
C	0.007	0.010	0.19	0.25
e	0.050 BSC		1.27 BSC	
E	0.150	0.157	3.80	4.00
H	0.228	0.244	5.80	6.20
L	0.016	0.050	0.40	1.27

VARIATIONS:

DIM	INCHES		MILLIMETERS		N	MS012
	MIN	MAX	MIN	MAX		
D	0.189	0.197	4.80	5.00	8	AA
D	0.337	0.344	8.55	8.75	14	AB
D	0.386	0.394	9.80	10.00	16	AC

NOTES:

1. D&E DO NOT INCLUDE MOLD FLASH.
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED 0.15mm (.006").
3. LEADS TO BE COPLANAR WITHIN 0.10mm (.004").
4. CONTROLLING DIMENSION: MILLIMETERS.
5. MEETS JEDEC MS012.
6. N = NUMBER OF PINS.

 DALLAS SEMICONDUCTOR			
PROPRIETARY INFORMATION			
TITLE: PACKAGE OUTLINE, .150" SOIC			
APPROVAL	DOCUMENT CONTROL NO. 21-0041	REV. B	1/1

High-Speed, Low-Power, 3V/5V, Rail-to-Rail, Single-Supply Comparators

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
8	12/08	Added SO package diagram and removed transistor count	10
9	3/09	Corrected <i>Ordering Information</i> for MAX944ESD	10

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